NUMBER SYSTEM

2.1 INTRODUCTION

We are familiar with the decimal number system which is used in our day-to-day work. In the decimal number system there are ten digits which are used to form decimal numbers. Ten separate symbols 0, 1, 2, 3, 4, 5, 6, 7, 8 and 9 are used to represent ten decimal digits. A digital computer stores, understands and manipulates information composed of only zeros and ones. A programmer (or user) who works on a computer is allowed to use decimal digits; letters A, B, C, ... Z, $a, b, c, \ldots z$, usual special symbols, +, -, etc. for his convenience. The decimal digits, letters, symbols, etc. are converted to binary codes in the form of 0s and 1s within the computer. To understand the operation of a computer the knowledge of binary, octal and hexadecimal number system is essential. This chapter deals with these number systems.

2.2 DECIMAL NUMBER SYSTEM

As the ten fingers of our hands are the most convenient tools nature has given, human beings have always used them in counting. So the decimal number system followed naturally from their use. The base or radix of a number system is defined as the number of digits it uses to represent numbers in the system. Since the decimal number system uses ten digits, from 0 to 9, its base or radix is 10. The decimal number system is also called base-10 number system. The weight of each digit of a decimal number depends on its relative position within the number. This is explained by the following example.

Example. Take the decimal number 6498 as an example to explain the weight of each digit of the number.

$$6498 = 6000 + 400 + 90 + 8$$
$$= 6 \times 10^3 + 4 \times 10^2 + 9 \times 10^1 + 8 \times 10^0$$

The weight of each digit of a decimal number depends on its relative position within the number as explained below:

The weight of the 1st digit of the number from the right hand side = 1st digit × 100.

The weight of the 2nd digit of the number from the right hand side = 2nd digit \times 101.

The weight of the 3rd digit of the number from the right hand side = 3rd digit \times 10².

The weight of the 4th digit of the number from the right hand side = 4th digit \times 10³.

The above expressions can be written in general form as follows: The weight of the nth digit of the number from the right hand side

= nth digit $\times 10^{n-1}$

= nth digit × (Base) $^{n-1}$

The number system in called **positional number system.** The above form of general en The number system in which the weight of each digit depends on its relative was a solution of connections of connections and connections of c

It is India that gave this positional method of expressing any number using ten any

2.3 BINARY NUI

IMBER SYSTEM

The weight of the 3rd bit of the number from the right hand side = 3rd bit \times 22.

The above expressions can be written in the form of a general expression given below. The weight of the 4th bit of the number from the right hand side = 4th bit $\times 2^3$. The weight of the nth bit of the number from the right hand side

= nth bit × 2^{n-1}

= nth bit × (Base)ⁿ⁻¹

each symbol receiving a value of position as well as an absolute value. It was a profon bove rule holds good for any other positional number system. The weight of a digit in any important idea. Its merit has been appreciated by a famous mathematican and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the position within the number and the base of the position of the positio It is seem unportant idea. Its ment has been appreciated by a famous mathematician Marquis dela ositional number system depends on its relative position within the numbers.

23 Binary number of decimal numbers. It is seen that this rule for a binary number is same as that for a decimal number. The

2.4 CONVERSION OF A BINARY NUMBER TO DECIMAL NUMBER

there is no symbol or digit to represent there is no difficulty in representing numbers a positional technique. Again, after 90 and therefore, it is represented by 10. It is sepresented by 10. It is sepresented by 10. a positional technique. Again, after 99 we have to represent hundred and utilizing positive to the second the To convert a binary number to its decimal equivalent we use the following expression:

positional technique it is written as 10. Three is written as 10. Three is written as 11. Again four is represent the seen the se by 1. There is no digit in the binary number system zero is representational technique it is written as 10. Three is system to represent two. Therefore, is represent to represent two is represent. epending on its position. The sum of the weights of all bits gives the equivalent number. The First we mark the bit position and then we give the weight of each bit of the number The weight of the nth bit of the number from the right hand side = nth bit $\times 2^{-1}$.

bllowing example illustrates the process.

Example 1. Convert the binary number 10 to its decimal equivalent.

binary number becomes very long and cumbersome. The weight of each binary bit of a but following example.

The very long and cumbersome. The weight of each binary bit of a but of a but of each binary bit of each bi The 2nd bit from the right hand side The first bit counting from the right hand side Its weight Its weight $1 \times 2^{2-1}$ The decimal equivalent = 0 × 21-1 = 0 × 20 = 1 × 21 + 0 × 20 = 1 × 21

Example 2. Convert the binary number 101 to its decimal equivalent. =2+0=2

as shown below. First we can write the binary number in the spread form and then show the bit position

number depends on its relative position within the number. It has been explained by number as explained below: The weight of the 2nd bit of the number from the right hand side = 1st bit - part of the 2nd bit x 21. The weight of the 1st bit of the binary number from the right hand side = 1st bit × 1. Example. Take the binary number 1101 as an example to explain the weight of each The weight of each bit of a binary number depends on its relative position within 1101 (Binary Number) = $1 \times 2^3 + 1 \times 2^2 + 0 \times 2^1 + 1 \times 2^0$

The binary number in the spread form = 1 0 Bit position from right hand side: 3rd 2nd 1

The weight of each bit is assigned and added together to give the decimal equivalence (binary number) = $1 \times 2^3 + 0 \times 2^4 + 1 \times 2^9$

* 4 + 9 + 1

= 6 (Decimal Number).

Example 8. Convert the binary number 1019 to its decimal equivalent. The binary number in the spread form = 1 0 1 0

But presistant from the right hand side: 4th 3rd 2rd 1s 1010 (Banery Bandsor) = $1 \times 10 \times 9 \times 10 \times 11 \times 10 \times 10 \times 10$

* 8 + 9 + 2 + 9

- 10 (Decimal Number).

The basery consider to the spread form = 1

But possibles from the right hand side: 5th 4th 3rd 2rd 1st 15th (Binner) Streether) = $1 \times 2^6 + 1 \times 2^6 + 0 \times 2^6 + 0 \times 2^5 + 1 \times 2^6$

= 16 + 8 + 0 + 0 + 1 = 25 (Decimal Number).

To indicate the base of a comber the following technique can be used

(26) to = (1001)

The suffix 10 indicates that the remiker 25 is a beamst number. The suffix 2 indicates that the remiker 11001 is a beamst number.

S CONVENSION OF A DECIMAL NUMBER TO A BRIARY WHILE

has descined received to be position from the right hand side is for 14, 2nd for 10s, hand side is for 14, 2nd for 10s, hand side is for 14, 2nd for 10s, hand side is for 15, 2nd for 1, 2

MALBER BARMUN

1 + 2 = 0	2 + 2 = 1	5 + 2 = 2	19 + 2 = 5	41 + 2 = 20 20 + 2 = 10	Example 1. Convert the decimal number 41 to its binary equivalent.
					the d
`	•	•	`		ecimal
I (MSB)	0	1	0	1 (L8B) 0	number 41 to its
Equivalent to dividing by 32. There is no 64 and one 32.	Equivalent to dividing by 16. There is one 32 and no 16.	Equivalent to dividing by 8. There are two life and one 8.	by 4. There are 5 eights and on 4	There are 20 twos and one 1. There are 10 fours and to 2.	binary equivalent,

Therefore, 41 (Decimal Number) = 101001 (Binary Number). The first remainder is the least significant bit (LSB) and the last remainder the most significant bit (MSB).

Example 2. Convert the decimal number 73 to binary.

+2 = (2 + 2 = 1	+ 2 = :	18 + 2 = 9	36 + 2 =	73 + 2 =	Suotte
)	_	~ *	9	2	8	nt
I CM SI	0 :	• 4	0	0 .	1 (1 S.2)	0
8				367)	amaer	

The decimal number 78 = 1001001 (Binary Number)

Checking of the answer.

The decimal equivalent of the above binary number is given by $1001001 = 1 \times 2^6 + 0 \times 2^6 + 0 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 0 \times 2^1 + 1 \times 2^6$

= 64 + 0 + 0 + 8 + 0 + 0 + 1 = 73 (Decimal Number) Example 3. Convert the decimal number 153 to binary.

Quotient

163 + 2 = 76

164 + 2 = 38

76 + 2 = 38

38 + 2 = 19

19 + 2 = 4

4 + 2 = 2

 $2 \div 2 = 1$ $1 \div 2 = 0$ 1 (MSB)

153 (Decimal Number) = 10011001 (Binary Number).

Checking of the answer.

Checking of the current.

Checking of the current.

$$10011001 = 1 \times 2^7 + 0 \times 2^6 + 0 \times 2^5 + 1 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 0 \times 2^1 + 1_{x_1}$$

$$= 128 + 0 + 0 + 16 + 8 + 0 + 0 + 1$$

= 153 (Decimal Number).

2.6 ADDITION OF BINARY NUMBERS

rules for binary addition. In the binary number system 1+0=1. When 1 is added to 1, the sum is 0 with a α 1. If the sum is written upto 2 bits, it is equal to 10 (2 decimal). The Table 2.2 shows:

Table 2.2 Binary Addition

the adjacent bits. When 1 is added vary 1. This carry is added to the sum

Example 1

		Example 3		Example 2	
↑ 10111	+ 1101	0101	TT00 +	0111	+ 0101
	(10 decimal number)		(7 decimal number)	(14 decimal number)	(9 decimal number)

NUMBER SYSTEM

2.7 BINARY SUBTRACTION

The following examples will illustrate binary subtraction.

Example 1

	1		
1001	10101	←	Borrow
(a decimal number)	(- 5 decimal number)		W

Exampl

0011 (- 3 decimal number) 0111 (7 decimal number)	0111	
(10 decimal number)	1010	Example 3
0101 (5 decimal number)	0101	
(- 5 decimal number)	- 0101	
(10 decimal number)	1010	Example 2
1001 (9 decimal number)	1001	
(- 5 decimal number)	- 0101	
1110 (14 decimal number)	1110	

Example 4

ve evemnles sme			
aller number	0110	-0111	1101
we examples smaller number has been subtracted from	0110 (6 decimal number)	-0111 (- 7 decimal number)	(13 decimal number)

us see what happens if a larger number is subtracted from a smaller "umber. In the above examples smaller number has been rom a larger number. Let

Example 1

0101 (5 decimal numl - 0111 (- 7 decimal numl 1110 (- 2 decimal numl						
(5 decimal numl (- 7 decimal numl (- 2 decimal num	Borrow	+	0101	- 0111	1110	
mber)			(5 decimal number)	(- 7 decimal number)	(- 2 decimal number)	

complement of 2. This will be explained in the next section. Example 2 The result is not a simple representation of - 2 i.e., -0010. The result is the 2's

		- DR.	A CONTRACTOR OF THE PARTY OF TH	
1111	- 1000	0111	*	Borrow
(-1 decimal number)	(- 8 decimal number	(7 decimal number)		

Carry

The result is 2's complement of 1.

9.8.3 Addition in 10's Complement System If 10's complement of a number is added to any number it is equivalent to its subtraction

The 10's complement of 21 = 78 + 1 = 79The 9's complement of 21 = 78

+ 79 1 65 86 (10's complement of 21)

Ignore carry

If the carry of the last stage is dropped, the result is correct.

xample 2. Add 59 and (- 84) The 9's complement of 84 =

The 10's complement of 84 = 15 + 1 = 16

+ 16 75 (10's complement of 25) (10's complement of 84)

get the correct result take 9's complement of the result, add 1 and put a - sign before There is no carry and the result is in 10's complement. It is the 10's complement of 25.

The 9's complement of 75 = 24

Result = - 25, which is the correct result The 10's complement of 75 = 24 + 1 = 25

cample 3. Add (- 26) and (- 43)

The 9's complement of 26 = 73

The 10's complement of 26 = 73 + 1 = 74

The 9's complement of 43 = 56The 10's complement of 43 = 56 + 1 = 57

1 31

economical because subtraction can also be performed using the same electronic anodes for the representation of decimal numbers. which is used for addition. In the binary number system there are two types of complex trample 1. Add 86 and (-21). is most widely used at present. To understand 1's and 2's complement in the binary me system one should first understand 9's and 10's complements in the decimal number as 1's complement and 2's complement. To represent a negative binary number 2's comple Most of the computers now perform subtraction using complemented number. If 10's complement of a number is auteu ways and in computers which employ BCD nomical because subtraction can also be performed using the same electronic from that number. 10's complement of a number is auteu ways and in computers which employ BCD that number is auteu ways and in computers which employ BCD nomical because subtraction can also be performed using the same electronic from that number.

2.8.1 9's Complement

subtracted from 9. The result so obtained is known as 9's complement of the number example, the 9's complement of 37 is (99 - 37) = 62. The 9's complement of 235 is (999 - 37) = 62. To form the 9's complement of decimal number each digit of a decimal number

2.8.2 10's Complement

The 10's complement of a decimal number is equal to the 9's complement of the number plus 1.

The 10's complement of 37 = 62 + 1 = 63. The 10's complement of a decimal number = Its 9's complement + 1.

The 10's complement of 235 = 764 + 1 = 765.

Now let us examine the sum of a decimal number and its 10's complement

8 (its 10's complement) (decimal number)

-Ignore carry

In other words the sum of a number and its 10's complement is equal to zero if the humber and its 10's complement is considered only upto two digits, the sum is equal to the words the sum of a number and only upto two digits, the sum is equal to the sum of a number and only upto two digits, the sum is equal to the sum of a number and only upto two digits. The above example the given decimal number is of two digits. If the sum and its 10's complement is a decimal number is of two digits.

+ 78 1 000 fits 10's complement) (decimal ne . ..

complement is considered only upto three digits. If the sum of a decimal number and a sample 4. Add 34 and 58 seen that the 10's complement represents it. the sum is equal to zero. Therefore The decimal number 235 is of three digits. If the sum of a decimal number and 3 maple 4. Add 34 and 58 ant of a decimal number - - decimal number ensent represents the negative value of the number.

required. The result is positive and correct. There is no carry at the last stage. As the both numbers are positive, they are simply added. The 10's complement

Conclusion

If the result is positive it is correct as usual. If the result is negative, it is in 10's comple Computer can represent signed numbers, and hence recognize whether the result is ju discussing 2's complement which is similar to 10's complement in the decimal system Whether the result is positive or negative the carry of the last stage is to be do

2.8.4 1's Complement

decimal system. To obtain 1's complement of a binary number each bit of the binary ne is subtracted from 1. For example, the 1's complement of the binary number 010 is 101 I's complement of 1110 is 0001. Thus I's complement of a binary number may be form simply changing each 1 to a 0 and each 0 to a 1. The 1's complement in the binary number system is similar to 9's complement Example 1. Find I's complement of the binary number 101100.

Example 2. The 1's complement of 101100 :: 010011

Find 1's complement of the binary number 0000. The 1's complement 0000 = 1111

Example 3. Find 1's complement of the binary number 1111. The 1's complement of 1111 = 0000

2's Complement

The 2's complement in the binary number system is similar to 10's complement in the decimal number system. The 28 complement of a binary number is equal The 2's complement of a binary number = Ita 1's complement + 1.

Example 1. Find 2's complement of the binary number 101100. The 2's complement of 101100 = 010011 + 1 = 010100

Find 2's complement of the bin--- number 0000.

Example 3. Find 2's complement of the binary number 111. The 2's complement of 6000 = 1111 + 1 = 10000

Let us examine the sum of a binary number and it. 2. on plement Example 1. Add the binary number 1100 and its 2's complement The 2's complement of 111 = 000 + 1 = 001

Its 1's complement = 0011

Its 2's complement = 0011 + 1 = 0100

The number + its 2's complement = 1100

_ ignore carry

form. So it converts it to get the result in the compiler and that it is in the complement is equal to zero. In the above example the binary number is of 4 bits. If a 4-bit form. So it converts it to get the result in the considered only unto 4 bits. form. So it converts it to get the result in the usual form. This has been explained processor is used and the last carry is neglected, the sum will be considered only upto 4 bits. For an 8-bit processor the binary number and its 2's complement is written upto 8 bits If the carry of the last stage is neglected the sum of a binary number and its 2's

as shown below in the examples 2 and 3. Example 2. Add the binary number 1011 and its 2's complement

Its 1's complement The binary number = 11110100 = 11110100 + 1= 00001011

The given binary number Its 2's complement = 00001011= 11110101

+ Its 2's complement + 11110101 1 00000000 _ Ignore carry

The sum only upto 8 bits is equal to zero.

Example 3. Add 5 and (- 5).

5 (decimal) = 00000101 (binary)

-5 = Its 2's complement = 11111010 + 1 = 11111011

00000101

+ (- 5) + 1 1 1 1 1 1 0 1 1 (+ 2's complement of 5) 100000000

If the sum is considered only upto 8 bits, it is equal to zero

Ignore carry

.8.6 Binary Subtraction Using 2's Complement

clear from the following example. The addition of 2's complument of a number is equivalent to its subtraction. This will

Example 1. Subtract 2 from 6.

Simple binary subtaction

- 0010 (- 2 decimal) 0110 0100 (4 decimal) (6 decimal)

Subtraction using 2's complement

I's complement of 0010 (2 decimal) = 1101

2's complement of 0010 (2 decimal) = 1101 + 1 = 1110

3
8
6
2
8
1
8
-

(+ Za complement of 2)

1 0100 (4 decimal)

- lguare carry

Example 2. Substruct 3 from 5

I's samplement of 9511 (3 decimal) . 1100

Zs complement of 0011 (3 decimal) = 1100 + 1

(5 decimal)

500 (2 decimal) (+ Zs complement of 3)

- liquine carry

The carry of the last stage is neglected.

2.8.7 Representation of Signed and Unsigned Numbers

9 is represented by 0 1001. To represent assprine number a 1 is placed before the se number. But there are three different ways to represent a negative number. These st adopted. To represent positive sign a 0 is placed before the binary number. For example ater than the negative number. computer such solution can not be employed and therefore, a different technique has a In the decimal system we put . or - sign before a number to represent its sign mple. -9 will be represented as 1 1001. There is only one way to represent a pa

- Signed-Z's Complement Representation Signed-I's Complement Representation

The representation of -9 in above three representations are shown below:

Signed-1's complement representation 19119

Signed-2's complement representation 10111

Hence, 9 as represent used to represent the sign of the number. For example an example of the number, and to represent the sign of the number. Hence, 9 is represented by 4 binary bits and a square bit is used to represent the sin of a square bit is used to represent the

10001001

Signed-1's complement representation 11110110

is used for sign representation it will be called unsigned representation When all the bits of the computer word are used to when all the bits of the computer word are used to when all the bits of the computer word are used to when all the bits of the computer word are used to when all the bits of the computer word are used to

2.8.8 Addition in Signed 2's Complement

The carry of the last stage is to be neglected if we are using To complement to be distinguish a positive number from a negative number. A 1 in the sign let is used to the different situations in Zs complement addition. dearly indicates whether the result is positive or negative. The following examples will show represent a negative number and a 0 in the sign bit a positive number. The use of sign bit rules also hold good for I's complement. A sign bit adjacent to the most significant bit is used decimal system. We have already discussed the rules for addition in 10's composited. These The Za complement in the binary number system is similar to "I've remisement in the

Example 1. Addition of two positive numbers.

Add 5 and 3.

+ 1000 9011 + 0101 Normal Notation (+ 8 decimal) (+ 3 decimal) (+ 5 decimal) 0 1000) (+ 8 decimal) 0 0101 Computer Notation (+ 3 decimal with sign bit.) (+ 5 decimal with sign bit.

10 in the sign bit indicates that the result is positive. There is no carry at the last stage or fifth bit from the right) is the sign bit. The addition is also performed on the sign bit. In be above example the sum 1000 is correct in the binary form and it is equal to 8 decimal In computer representation sign bit has also been included. The first bit from the left

Example 2. Addition of a positive and a negative number, the positive number being

Add 9 and (-4).

The I's complement of 0100 (4 decimal) = 1011

The Z's complement of 0100 = 1011 + 1 = 1100

Normal Notation

Computer Notation

0101 0100 + 1001 (+ 9 decimal (5 decimal) - 4 decimal 100101 1 1100 0 1001 Neglect carry (+ 9 decimal with sign bit) (+ 5 decimal) (2's decimal of 4 with sign bit)

dicates the sum is positive. There is any at the last stage. The carry is to be dropped Example 3. Addition of a positive and negative number, the negative number being The sum 0101 is correct in the binary form and equal to 5 decimal. The sign bit 0

eater than the positive number.

Add (-9) and 3

7.4 1's complement of 1001 (9 decimal) = 0110

The 2's complement of 1001 = 0110 + 1 = 0111

-9 1 0111 (2's complement of 9 with sign bit.)

- 3 0 0011 (+ 3 with sign bit)

1 1010 (2's complement of 6 with sign bit)

arry at the last stage. The sum is negative as indicated by the sign bit. It is in 2's complete ent. There is no

adopted to get it is the correct form. When the result is negative and in 2's complement the fullowing proveding

the result in 2's complement is 1010. The correct result will be: Take 1's complement of the result, add 1 and put a - sign before it. In the above

The 1's complement of 1010 * 0101

(= 6 decimal)

-0110

Example 4. Add (- 12) and (- 2).

The 2's complement of 1100 The 1's complement of 1100 (12 decimal) - 0011 + 1

The 1's complement of 0010 (2 decimal)

0100

The 2's complement of 0010 101 1101 +

- 1110

Computer Notation

1 0100 carry = 1 (2's complement of 14 with sign bit) (2's complement of 2 with sign bit) (2's complement of 12 with sign bit)

The sum is negative and it is in 2's complement. There is a carry through the se

2's complement of 0010 (result) = 1110 The above result is in 2's complement. The correct result can be obtained as

- - 14 (decimal)

If the sign bit is 1, the numbers example will illustrate the addition of signed binary numbers are represented.

14 and +9by 7 bits. The MSB represents the sign of the number If the sign bit is 1, the number is negative. The negative also bit is 0, the number is percented as: by 7 bits. The MSB representation is negative. The negative number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number, is per lift the sign bit is 0, the number is negative. magnitude of a number. Consider an 8-bit computer. The magnitude of the word will represent the sign of the number if the sign till de of the number is represented to the sign of the number is represented to the sign till the In the above example, and the rest of the bits of the data word will represent In the above examples we have taken a separate sign in this chapter in Sec. 2. Convertion of A Chapter for a consider an 8-bit community of the bits of the computer the most sign.

00010111 00001001

Sign bit is 0, so the sum is positive.

Example 6, Add + 14 and (= 9).

100000101 (+ B desimal). 11110111 (3's complement of 9 with sign bit) 0001110

E High bit is 0, so the result is positive Carry = 1, it is neglected

Example 7. Add (- 14) and + 9. 11110010 (2's complement of 14 with sign bit)

1001000 00000100 1111011 (2's complement of 5 with sign bit) Bign bit is 1, so the result is negative. I's complement of the result

00000101(-5 decimal)

Prefix - is to show the result in usual standard binary form

Example 8. Add (= 14) and (= 9). 14 00010110 I's complement of the result. 11110111 (2's complement of 9 with sign bit) 11110010 (2's complement of 14 with sign bit) 111101001 (2's complement of 23 with sign bit) Sign bit is 1, so the result is negative Carry is 1, it is neglected

.00010111 (- 23 decimal)

The carry of the last stage is to be dropped. When the sum is positive, it is in the first the sum is positive or negative it is in 2s complement. The sign bit indicates spainting in the sign bit clearly indicates whether the result is positive or negative. If it the correct form for display by the computer very easily. This is true only when there are not overflow it on the screen. The case of overflow is discussed the first only when there are not overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first of the case of overflow is discussed the first overflow in the case of overflow is discussed the first overflow in the case of overflow is discussed the first overflow in the case of overflow is discussed to the first overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in the case of overflow is discussed to the first overflow in t

In the decimal number system the weights of the digits which come after the decima

0.635 = 0.6 + 0.03 + 0.005

* 6 × 10-1 + 8 × 10-9 + 5 × Supati Sh Omarga HABBIT

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the binary point, can be expressed as 01101 * 1 * 2 · 1 * 2 · 0 * 2 · 1 * 2 ·

Example 1. Convert the binary fraction 0.101111 to decimal fraction 0.8125 (docimal)

0.10111 =
$$1 \times 2^{-1} \times 0 \times 2^{-2} \times 1 \times 2^{-3} \times 1 \times 2^{-4} \times 1 \times 2^{-4}$$

= $1 \times \frac{1}{2} \times 0 \times \frac{1}{4} \times 1 \times \frac{1}{16} \times 1 \times \frac{1}{16} \times 1 \times \frac{1}{32}$
= $0.5 \times 0 \times 0.125 \times 0.0626 \times 0.03125$

Example 1. Convert the binary real number 1101.1010 to a decimal real number 0.71875 (decimal).

equivalent is determined for both integer as well as fraction and these are added to A binary real number consists of two parts an integer and a fraction. The ac-

2.10 CONVERSION OF A DECMAL PRACTION TO A BINARY FRACTION

and the remainder new fraction is used for the makiplication by 2 at the next star cation by 2 is used. The integer part is noted from after the multiplication by 2 at an Example 1. Convert the decimal fraction 0.5125 to a To convert a decimal fraction to its binary equivalent a technique of successive r

(decimal) = 0	090 980 9800 98180
5 (decimal) = 0.1101 (Binary)	1.00 0.50 0.50
	EEEE
	Grant Dinary fraction of the Control

0.8128

5 &	Traction - 1

0.64	0.89	0.16	0.08
100	0.64	0.33	97.0
098	0.04	0.20	0.10
1 (1000)	•	•	

continue further. For such a case an approximation is made. For this example, we may take the result up to 6th binary bit after the binary point. In this example it is seen that the fraction has not become zero, and the process will

CHAPTER 2

Example 3. Convert the decimal real number 12.625 to an equivalent binary real number

well as fraction separately For the above decimal real number the binary equivalent is obtained for both integer as

12 (decimal) is first converted to its binary equivalent

	-	50	0	-	
		+		843	D
-	62	160	90	*	£
e .				100	ž.
Acres -	1 + 2 = 0 1 (MSB)	1	Sa	œ.	end
100	_		_	_	_
None of the last	(MSB)	_	9	(MRAT)	Remainder

12 (decimal * 1100 (binary).

Then the decimal fraction 0.625 is converted to its binary equivalent

888	Practices
585	2 × Praction
	Remainder New Proctors
O (GEW)	Integer

0.625 (decimal) = 0.101 (binary).

Now adding the binary equivalents of 12 and 0.625, we get

12.625 (decimal) = 1100.101 (binary)

2.11 SHARY COOED DECMAL (BCD COOE

represented by its 4-bit binary equivalent. For example, 56 is represented by 0101 0110. If a decimal number consists of two or more than two digits, each decimal digit is individually decimal number is represented by four binary bits. For example, 3 is represented by 9011 The BCD is the simplest binary code to represent a decimal number. In BCD code

in BCD code as 0100 0011 For example, the binary equivalent of the decimal number 43 to 101011, but it is represented between a binary equivalent of a decimal number and the hinary code of a decimal num Numbers are usually represented by some sort of binary codes. There is a difference

Table 2.3 shows the standard HCD codes for one-digit and two-digit decimal in NUMBER SYSTEM

Table 2.3, Standard BCD Codes

100						10		•					•		•	0	District No.
1001 1001	1000 0100	OTTO TOTO	0001 00	00 1 00	0001 00	0001 0			0	0	0						aber Stander
	00	10	1100	0010	1000	0000	1001	1000	0111	110	1010	0100	1100	0010	1000	0000	idural BCD code

excess-3 code will be 0101 1000 0110. The drawback of this code is that it is not a wellexcess-3 code will be 0101 1000 0110. The drawback of all excess-3 code, 253 (decimal) in excess-3 code will be 0101 1000 0110. The drawback of all excess-3 code, 253 (decimal) in excess-3 code. In the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent an individual to the standard BCD code the weights of four binary bits which represent the standard BCD code the

into or out of a digital system.

electronic counters, digital clocks etc. work with BCD numbers, oltmeters, frequency complete do not use BCD numbers, BCD codes have also BCD codes are used where Electronic calculators, digital volumeters, digital clocks etc. work with BCD numbers, frequency south aple, 5 is vava,

BCD codes are used where the decimal information is directly (in coded form) transfer that the coded form) transfer that the coded form is directly (in coded form) transfer that the coded form) transfer that the coded form is the coded form transfer that the coded form the coded form the coded form that the coded form the coded

In addition to difficulty in standard BCD, if the range subjection there is difficulty in performing addition in standard BCD, if the range subjection there is fitness in the range 10 to it range 10 to electronic counters, digness computers do not use BCD numbers, BCD codes have also used in early computers. Modern computers do not use BCD numbers as they have to pro-In addition to difficulty in forming complements for binary subtraction there is

		18	÷	3		Hearing 6
						example 1, Add 8 and 5.
0	+					5
1100100	0110	1101	1010	1000	BCD	
Correct BCD 13	Add 6	Incorrect BCD				
BCD 13		HCD				

Example 2, Add 8 and 9.

		177	+ 10	8	
00010111	+ 0110	100010001	1001	1000	BCD
Correct BCD 17	Add 6	Incorrect BCD			

2.12 HEXADECIMAL NUMBER SYSTEM

bexadecimal number system 10 is represented by A, 11 by E, 12 by C, 13 by D, 14 by E and 15 by F. The decimal number 16 is represented by 10, 17 by 11 and so on base) is 16. Its digits from 0 to 9 are same as those used by decimal receiver system. In Sec. The hexadecimal number system is now extensively used in computer industry. Its radio

as Excess-3 code; 2,4,2,1 code etc. have been used in difficulty other BCD codenumbers and their binary representations, the reason for using 4 binary bits to written. For example, 5 (decimal) = 1000 (8 decimal) in their binary equivalent operations a hexadecimal digit is that the largest hexadecimal digit is consistent to largest hexadecimal digit. Transfer only 4 binary excess-3 code will be 0101 1000 0110. The decimal) in their binary equivalent for its representation. an acceptable BCD code in this system. To overcome this difficulty other BCD codespunded by 1000 0110, DE By 0101 1110, OE BY There is difficulty in forming complements when numbers are represented by studietts, each digit is represented individually by four binary bits. For example, 26 is represented by studietts, each digit is represented individually by four binary bits. For example, 26 is represented by studietts, each digit is represented individually by four binary bits. For example, 26 is representable BCD code in this system. To overcome the individual policy of the property of the propert digit is 8, 4, 2, 1. At present modern computers bits which represent an indiv. A hexadecimal digit is represented individually by four binary bits. For example, 36 is represented individually by four binary bits. For example, 36 is represented individually by four binary bits. For example, 36 is represented individually by four binary bits.

code, that is the sum of weights of binary bits is not equal to the corresponding beentation. The binary equivalent of a decimal number 94 is 1011110. The hexadecimal Another BCD code is 2.4.2.1 and 1. Another BCD code is 2, 4, 2, 1 code. It is a weighted code and it has complement. Table 2.4. Hexadecimal Numbers and their Binary Representations are identical.

Table 2.4. Hexadecimal Numbers and their Binary Representations.

Class Section 1997	Tanana.	Menadesimal Number	Blingry Cooked Blancableria real Number
00100	•	•	0000
			6000
			CT CT

	P.F.	200
		200
	58	2
	25	13
	25	37
	10	
		16
_		18
		=
		16
_		•
-	•	•
Si Si		

2.12.1 Hexadecimal Versus BCD

thereby reducing the hardware cost. The arithmetic operations are also simpler in her the hexadecimal is a compact form of representation, and it occupies less memory to FF, that is 11111111 (255 decimal) whereas in BCD only up to 10011001 (99 decimal) binary codes from 1010 to 1111. In the hexadecimal system an 8-bit word can represent binary bits, whereas BCD codes do not utilize the same. The BCD codes do not utilize From Table 2.4 it is clear that the hexadecimal system utilizes the full capacity

memory dumps in hexaderimal as printouts that are very compact. Listings are also preform which is very compact and easy to examine. Computers which work in binary processing the computers which were computers where computers were computers which were computers where computers were computers were comput very difficult to examine. Therefore, the stored information is printed out in heart information stored in the memory. The memory stores information in binary form When a computer gives hardware or software error it may be required to

efficient if large input-output. The computers using BCD codes perform and small number of computerinal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) and eliminate the requirement for conversion to binary and decimal data (BCD codes) are decimal data (BCD codes) and decimal data (BCD codes) and decimal data (BCD co efficient if large input-output requirements are involved and small number of computers. are to be conversed we have number of computations. A processor working with BCD processor (hexadecimal or octal) performs arithmetic operations the input data have Usually, the input data and the result (output) are in the decimal form. Before of have

ers dealing with accounting appured to the exact representation of decimal numbers including the desired for the exact representation of decimal numbers including the reference, BCF arithmetic takes more time for execution and have the digital after the Conversion of a decimal number is divided by 16 successively. Many decimal fractions on the exact representation of decimal numbers including the business of the exact representation of decimal numbers including the business of the exact representation of time for execution and thing the therefore, BO Many decimal fractions do not have exact binary equivalent to sum business of

arithmetic. Thus the price paid for precise decimal fractions is less efficient use of memory and slower processing.

calculations with both binary (hex.) as well as BCD. use hexadecimal system. Some large computers may have the hardware facility for arithmetic Some computers and all calculators employ BCD codes. Most of the modern computers

2.12.2 Conversion of a Hexadecimal Number to a Decimal Number

following well known expression for the weights of digits of the number will be used: For the conversion of a hexadecimal number to an equivalent decimal number the

The following examples will illustrate the procedure. The weight of nth digit of the number from the right hand side = nth digit × (Base)ⁿ⁻¹

 $3 \times 16^{\circ}$. 8 is the 1st digit of the number from the right hand side, therefore, its weight is Example 1. Convert the hexadecimal number 4B8 to its equivalent decimal number.

B × 161 B is the 2nd digit of the number from the right hand side, therefore, its weight is

4 × 162. 4 is the 3rd digit of the number from the right hand side, therefore, its weight is

Therefore, 4B8 (hex.) =
$$4 \times 16^2 + B \times 16^1 + 8 \times 16^0$$

$$= 4 \times 256 + 11 \times 16 + 8 \times 1$$

$$= 1024 + 176 + 8$$

= 1208 (decimal).

Example 2. Convert the hexadecimal number 6E to its decimal equivalent.

$$6E \text{ (hex.)} = 6 \times 16^1 + E \times 16^0$$

6E (hex.)

$$= 6 \times 16 + 14 \times 1$$

$$= 94 + 14$$

Example 3. Convert the hexadecimal number 2B6D to its equivalent decimal number 2B6D (hex.)

$$= 2 \times 16^{3} + B \times 16^{2} + 6 \times 16^{1} + D \times 16^{0}$$
$$= 2 \times 4096 + 11 \times 256 + 6 \times 16 + 13 \times 1$$

= 11117 (decimal).

12.3 Conversion of a Hexadecimal Fraction to a Decimal Fraction

oint are as follows: In the hexadecimal system the weights of the hexadecimal digits after the hexadecimal

=
$$5 \times 16^{-1} + A \times 16^{-2} + 6 \times 16^{-3} + B \times 16^{-4}$$

= $0.3125 + 0.0390625 + 0.0014648437 + 0.00016784667$

.12.4 Conversion of a Decimal Number to a Hexadecimal Number

For the Conversion of a decimal number to an equivalent hexadecimal number, the

Example 1. Convert the decimal number 67 to an equivalent hexadecimal

 $67 \div 16 = 4$ Remainder

 $4 \div 16 = 0$

The decimal number 67 = 43 (hexadecimal).

Example 2. Convert the decimal number 952 to its hexadecimal equivalent

 $952 \div 16 = 59$ $59 \div 16 = 3$ $3 \div 16 = 0$ 11 = B Remainder

The decimal number 952 = 3B8 (hexadecimal).

2.12.5 Conversion of a Decimal Fraction to a Hexadecimal Fraction

multiplication and the new remainder fraction is used for multiplication at the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number expected in the near shared ecimal number to its equivalent binary number expected in the near shared ecimal number to its equivalent number nu For the conversion of decimal fraction to its equivalent hexadecimal fraction the

xadecimal point	21.0	20.02	20.0	0.72	0.92	0.62
irther continu	1.92	6.12	8.32	11.52	14.72	900
0.82	0.12	0.32	0.52	0.72	0.92	Kemainde
1 (LASD)	•		=	<u>=</u>	9 (MSD)	Integer

The decimal fraction 0.62 = 0.9BB851 (hex.) approximately. ereiore, the result has been taken upto OP

2.12.6 Conversion of a Binary Number to a Hexadecimal Number

The base of the hexadecimal number system is 16. To convert a binary groups (each group contains 4 binary humber uses a very sumper to hexade The conversion of binary number to hexadecimal number uses a very simple led

4-bit binary groups (each group contains 4 binary bits) are forming the groups, each group of 4 binary bits are formed in the binary number to make the binary number to make

umber may be grouped as follows: nly 2 binary bits. This can be extended to 4 binary bits by adding zeros in MSB positions MSBs are extended by adding zeros the number remains unaffected. Therefore, the given In the above expression we see that the group of the most significant binary bits contains

 $(1101001101)_2$ = 34D (hex.)= (0011)(0100)(1101)

ecimal real number. Example 3. Convert the binary real number 1011100. 1000101 to its equivalent hexa-

inary point, is made from left to right. For the integer part group is made from right to the In case of binary fraction, the formation of grouping of binary bits which are after the

1011100.1000101 = (101)(1100)(1000)(101)

= (0101)(1100)(1000)(1010)

Example. Convert the decimal fraction 0.62 to its equivalent hexadecimal fraction fraction 0.62 to its equivalent hexadecimal fraction fraction 0.62 to its equivalent. To convert a hexadecimal number to its equivalent binary number each digit of the given

Example 1. Convert the hexadecimal number 6B9 to its equivalent binary number.

= (0110)(1011)(1001)

 $= (110101111001)_2$ $= (0110101111001)_2$

Example 2. Convert the real hexadecimal number 6D.3A to its equivalent binary number

= (0110)(1101)(0011)(1010)

= (01101101.00111010)

= (1101101.0011101)

13 OCTAL NUMBER SYSTEM

Example 1. Convert the binary bits) are formed in the binary number to hexample 7 which can be represented by only 3 binary bits 1.6., 7 = 111. If an occas number (01101110)

= (0110) (1110)

= am a_____ to the equivalent hexadecimal right and their binary representations. The binary representation of an actal number is absented by a group of three binary representations. The binary representation of an actal number is absented by a group of three binary representations. The binary representation of an actal number is a set of the property of the property of the property is absented by the property of the binary representation of an actal number is a set of the property of the uses eight digits 0, 1, 2, 3, 4, 5, 6 and 7. The decimal number 8 is represented by 10, 9, 11, 10 by 12 and so on in the octal number system. An octal number is represented by The base of the octal number system is 8. This system is also used in computer industry.

The formation of groups of 4 bits each in an integer binary equivalent hexades!" tet us examine the Binary equivalent and the decimal number as in the control of the binary equivalent of the decimal number as in the control of the binary equivalent of the decimal number as in the control of The formation of groups of 4 bits each in an integer binary binary binary better the binary representations or a factorial in state to left.

Let us examine the binary equivalent and binary representations or a factorial side to left.

Let us examine the binary equivalent and binary equivalent and binary equivalent and binary equivalent of the decimal number de factorial in the transmission of the binary equivalent and binary equivalent

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sion of an Octal Number to a Decimal Number

make the use of the following well known expression: For the conversion of a number from any number system to decimal number system

Example 1. Convert the octal number 56 to its equivalent decimal number. The weight of the n-th digit of the number from right hand side = nth digit × (Bus 3.4 Conversion of a Decimal Fraction to an Octal Fraction to the octal fraction to the octal fraction to the equivalent octal fraction.

= 244 (decimal)

2.13.2 Conversion of an Octal Fraction to a Decimal Fraction In the octal system the weight of the octal digits after the octal Point are as exp

 $0.563 \text{ (octal)} = 5 \times 8^{-1} + 6 \times 8^{-2} + 3 \times 8^{-3}$

MBER SYSTEM

= 0.625 + 0.09375 + 0.005859375

= 0.724609375 (decimal)

13.3 Conversion of a Decimal to an Octal Number

repeated division by 8 is used. For the conversion of a decimal number to an equivalent octal number, the technique

Example 1. Convert the decimal number 62 to its equivalent octal number.

7 + 8 = 0	62 + 8 = 7	
7 (MSD)	6 (LSD)	Kemainder

Example 2. Convert the decimal number 958 to its equivalent octal number.

62 (decimal)

76 (octal)

1676 (octal)	н	958 (decimal)
1 (MSD)		1 + 8 = 0
6		14 + 8 = 1
7	,	119 + 8 = 14
6 (LSD)		958 + 8 = 119
Remainder		Quotient

Checking the answer:

$$1676 \text{ (Octal)} = 1 \times 8^3 + 6 \times 8^2 + 7 \times 8^1 + 6 \times 8^0$$
$$= 512 + 384 + 56 + 6$$
$$= 958 \text{ (decimal)}$$

As the base of the octal number system is 8, applying the general rule of convepeated multiplication by 8 is used. The integer part is noted down and new remainder system is 8, applying the general rule of convepeated multiplication by 8 is used. The integer part is noted down and new remainder system is 8, applying the general rule of convepeated multiplication by 8 is used. The integer part is noted down and new remainder system is 8, applying the general rule of convepeated multiplication at the next stage.

Example 1. Convert the decimal fraction 0.96 to its equivalent octal fraction.

0.52 4.16 0.16 1.28				Fraction Fraction × 8 F
0.28	0.52	0.44	0.68	Remainder Fraction
1(LSD)	. •		7 (MSD)	New Integer

The process will continue further so we may take the result upto 5 places of octal point. 0.96 (decimal) = 0.75341 (octal) approximately

13.5 Conversion of a Binary Number to an Octal Number

3 binary bits is converted to its octal equivalent. binary bits each are formed in the binary number. After forming the groups, each group The octal number system is a base-8 system. For binary to octal conversion, groups of

The formation of groups of 3 bits each in an integer binary number is make Example 1. Convert the binary number 101110 to its equivalent orlation

 $(101110)_2 = (101)(110)$

* 56 (octal)

Example 2. Convert the binary number 1101011 to its equivalent octal number $(1101011)_2 = (1)(101)(011)$

As the leftmost group consists of only one binary bit, this group is extended by

 $(1101011)_2 = (001)(101)(011)$

= 153 (octal)

In the binary fraction the group of 3 bits is formed from left to right. Example 3. Convert the binary real number 1011.1011 to its equivalent odals In the integer part of the binary number the group of 3 bits is formed from figure

 $(1011.1001)_2 = (1)(011)(101)(1)$

= (001)(011)(101)(100)

2.13.8 Conversion of an Octal Number to a Binary Number

number is converted to its 3-bit binary equivalent. To convert an octal number to its equivalent binary number each digit of the pre Example 1. Convert the octal number 376 to its equivalent binary number

 $(376)_8 = (011)(111)(110)$

= (011111110)₂

Example 2. Convert the real octal number 56.34 to its equivalent binary number 56.34 to its equivalent binar (56.34)₈ = (101) (110).(011) (100)

= (101110.011100)₂

easily be done through binary as illustrated by the reample given below.

Now the above binary equivalent is divided into groups of 3 bits to obtain its octal equivalent is divided into groups of 3 bits to obtain its octal equivalent is octal equivalent. (001)(110)(111)(100) = (11110111100 = 1736 (octal)

Recomple 2. Convert the real heradefimal number 58 th is the equivalent less sumber The number 58.34 is first converted to its singery equivalent

38.34 × (0101) (1011) (011) (1010)

0101100110011010 *

Now forming the groups of 3 binary bits to obtain its octal equivalent we have

01011011.00111010 = (01) (011) (011).(001, (110) (10) * (001) (011) (011),(001) (110) (100)

* 133.184 (octal)

Converting 536 (octal) first to its binary equivalent, we get Example 3. Convert the octal number 508 to its equivalent hexadecimal number

 $(538)_8 = (101)(011)(110)$

Now forming the groups of 4 binary bits to obtain its hexadecimal equivalent we get, = (101011110)₂

(1010111110) = (1)(0101)(1110)

= (0001) (0101) (1110)

= 15E (hex)

Converting 46.57 (octal) first to its binary equivalent we get, Example 4. Convert the real octal number 46.57 to its equivalent hexadecimal number.

46.57 = (100)(110)(101)(111)

Now forming the groups of 4 binary bits to obtain its hexadecimal equivalent we have, = (100110.101111)2

100110.101111 = (10) (0110).(1011) (11)

= (0010) (0110) (1011) (1100)

= 26.RC (hex)

L14 ASCII AND ISCII CODES

The conversion of a hexadecimal number to a Hexadecimal Number and Vice manufacturers. It is a 7-bit code. Microcomputers using 8-bit word length use 7 bits to 7. Table The hexadecimal number 3DE is first converted to it. equivalent octal number, special characters and control characters. Table 2.7 shows the definitions of numbers, special characters and control characters. Table 2.7 shows the definitions of numbers, special characters and control characters. Table 2.7 shows the definitions are: The conversion of a hexadecimal number to a Hexadecimal Number and Vice vammunication devices, it has reconstructed by the equivalent octal number or vice present the basic code. The 8th bit is used for parity or it may be permanently 1 or 0. Table to have a silustrated by the remainded number or vice present the basic code. With 7 bits up to 128 characters can be coded. A letter, digit or special int, used to indicate errors in reception. IQ, ACK, NAK, etc. ENQ is used for enquiry. ACK is for acknowledgement. It is used to licate successful reception after completing error checking. NAK is negative acknowledge-

tian languages. ISCII stands for Indian Standard Code for Information Interchange. It is an 8-bit code for